



"METHODS AND APPARATUS FOR ALIGNING AN INTEGRATED  
CIRCUIT PACKAGE WITH AN INTERFACE"

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Serial No. - 09/915,076

Docket No. - 10010311-1

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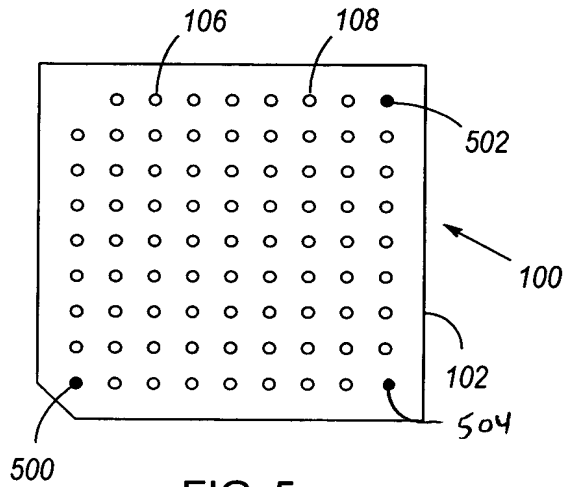


FIG. 5

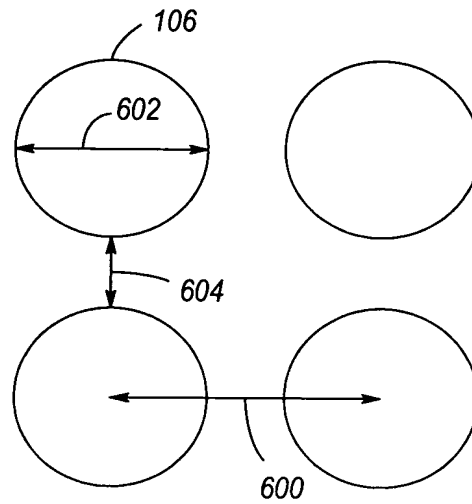


FIG. 6

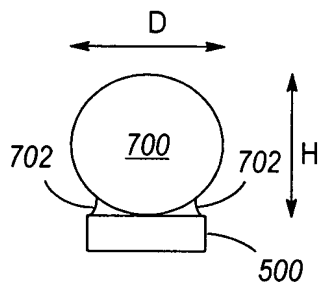


FIG. 7

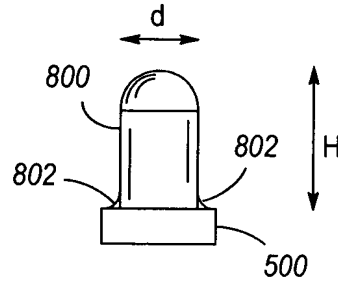


FIG. 8

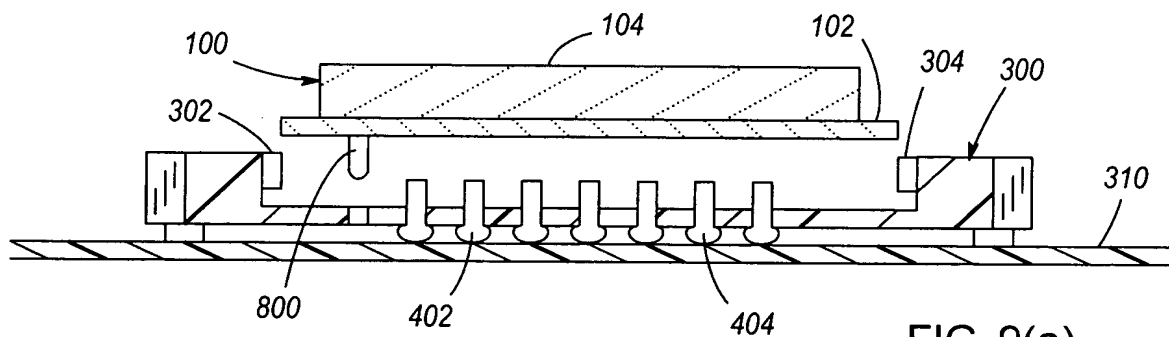


FIG. 9(a)

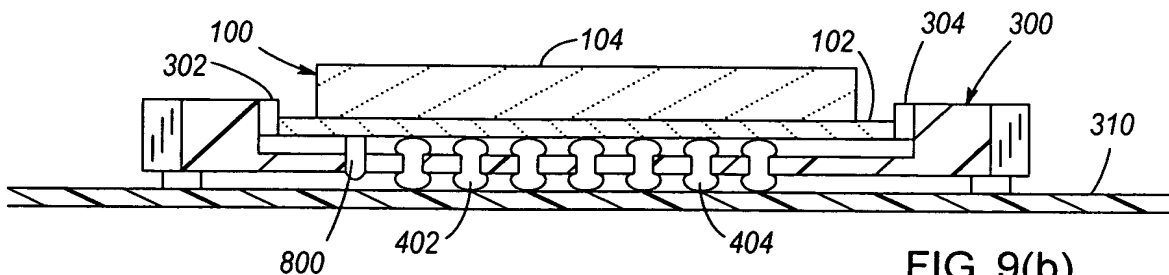


FIG. 9(b)